

B.Tech II Year - II Semester Examinations, April/May-2012
OPERATING SYSTEMS & SYSTEM PROGRAMMING
 (COMPUTER SCIENCE AND ENGINEERING)

Time: 3 hours

Max. Marks: 80

Answer any five questions
All questions carry equal marks

- - -

- 1.a) Differentiate between UNIX OS and Windows OS.
 b) Suppose that the following processes arrive for execution at the times indicated. Each process will run the listed amount of time. In answering the questions, use preemptive and non-preemptive scheduling and base all decisions on the information you have at the time the decision must be made.

Process	Arrival Time	Burst Time
<i>P1</i>	0.0	8
<i>P2</i>	0.4	4
<i>P3</i>	1.0	1

What is the average turnaround time and average waiting time for these processes with the FCFS, SJF, SRTF, Priority and RR (Time Slice = 2ms) scheduling algorithms? [16]

- 2.a) What is process state diagram and what do they represent?
 b) What are the various processes involved in a typical context switch? Explain with a neat diagram. [16]
- 3.a) Compare process and a thread.
 b) What are the principles of concurrency?
 c) Explain how semaphores and monitors are used to achieve synchronization. [16]

- 4.a) Consider the following snapshot of a system

<u>Processes</u>	<u>Allocation</u>	<u>Max</u>	<u>Available</u>
	<i>A B C D</i>	<i>A B C D</i>	<i>A B C D</i>
<i>P0</i>	0 0 1 2	0 0 1 2	1 5 2 0
<i>P1</i>	1 0 0 0	1 7 5 0	
<i>P2</i>	1 3 5 4	2 3 5 6	
<i>P3</i>	0 6 3 2	0 6 5 2	
<i>P4</i>	0 0 1 4	0 6 5 6	

Answer the following questions using the banker's algorithm:

- i) What is the content of the matrix *Need*?
 ii) Is the system in a safe state?
 iii) If a request from process *P1* arrives for (0, 4, 2, 0), can the request be granted immediately?
 b) What are the necessary conditions for deadlock occurrence? [16]
- 5.a) What are the requirements for memory management? Explain paging technique with neat diagram.
 b) Write the syntax and purpose of `malloc()`, `calloc()` and `free()` system calls. [16]
- 6.a) What are the motivations for structuring file system hierarchically?
 b) Explain briefly about Record blocking and File sharing. [16]

7. Write a short note on the following: [16]
a) I/O Buffering b) Disk Caching c) RAID Architecture.
- 8.a) Explain the design of a two pass assembler with relevant data structures and symbol tables associated with it.
b) What is the need of a Linker, a Loader and a Macro Processor? [16]

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	<i>A</i>	<i>B</i>	<i>C</i>	<i>D</i>	<i>A</i>	<i>B</i>	<i>C</i>	<i>D</i>	<i>A</i>	<i>B</i>	<i>C</i>	<i>D</i>
<i>P0</i>	0	0	1	2	0	0	1	2	1	5	2	0
<i>P1</i>	1	0	0	0	1	7	5	0				
<i>P2</i>	1	3	5	4	2	3	5	6				
<i>P3</i>	0	6	3	2	0	6	5	2				
<i>P4</i>	0	0	1	4	0	6	5	6				

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