

II/II CSE

Code No: 09A40501

**R09**

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY, HYDERABAD**

**B.Tech II Year II Semester Examinations, June-2014**

**COMPUTER ORGANIZATION**  
**(Computer Science and Engineering)**

**Time: 3 hours**

**Max. Marks: 75**

**Answer any five questions**  
**All questions carry equal marks**

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- 1.a) Distinguish between multi processors and multicomputers.  
b) What are error detection codes? Explain them with suitable examples.
- 2.a) Explain logic micro operations and shift micro operations.  
b) Explain various addressing models.
3. Explain the design of control unit and hard wired control.
4. State various multiplication algorithms and explain any one of them with a suitable example.
5. What is meant by cache memory? Explain read-only memories and semi conductor RAM memories.
6. Explain the RS 232, USB and IEEE 1394 standard serial communication protocols.
7. What is meant by parallel processing? Explain the instruction pipeline and array processors.
8. Write short notes on the following:
  - a) Interprocessor arbitration
  - b) PCI
  - c) STACK organization.

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